

**REMARKS**

In response to the Office Action mailed November 5, 2003, claims 1-12 remain active in this application, of which claims 1 and 9 are independent. Based on the following Remarks, Applicants respectfully request that the Examiner reconsider the outstanding objections and rejections and they be withdrawn.

*Rejections Under 35 U.S.C. §103*

In the Office Action, claims 1-12 have been rejected under 35 U.S.C. §103(a) as being unpatentable over U. S. Patent No. 6,388,651 issued to Kinoshita, *et al.* ("Kinoshita") in view of U.S. Patent No. 5,973,660 to Hashimoto ("Hashimoto"), further in view of U.S. Patent No. 6,049,322 to Yoshikawa *et al.* ("Yoshikawa"). This rejection is respectfully traversed.

Kinoshita is directed to a flat-panel display having a plurality of horizontal pixel lines each formed of display pixels arranged in one line and composed of a first and a second driver groups arranged to divide the pixels into two groups and drive the two groups. A drive circuit board includes a gate-array control section connected to the first and second driver groups by a first and second wiring lines separated electrically. Hashimoto discloses a liquid crystal display including a gray level voltage and a gray level generator.

In order to reject a claim under 35 U.S.C. §103(a) there must be some suggestion or motivation, either in the reference themselves or in knowledge generally available to one of ordinary skill in the art, to modify the reference or to combine the reference teachings. Second, there must be a reasonable expectation of success. Finally, the prior art reference (or references when combined) must teach or suggest all of the claimed limitations.

Applicant submits that all the claimed features are not taught or suggested by the references, either singly or when combined.

The Examiner agrees that neither Kinoshita nor Hasimoto teach or suggest simultaneous outputting of a data signal from the first and second image signal, one of which is the left-side of the signal processor and the other of which is on the right-side of the processor as recited by claims 1 and 9. However, Applicants disagree with the Examiner that Yoshikawa supplies the missing features of Kinoshita and Hasimoto. Specifically, the Examiner asserts that Yoshikawa teaches outputting a first and a second image signal simultaneously and cites col. 2, lines 9-25 and 34-36. However, this section does not teach, at least, outputting of a first and a second image signal being output simultaneously. Rather it teaches that the FIFOs are individually “allowing itself to be read out and written into simultaneously” (lines 14-15), i.e., a read and write of the same image signal. This is not the same as a first image signal and a second image signal being written simultaneously. The invention of Yoshikawa actually teaches sequential outputting of signals at col.3, lines 18-35, specifically, at line 28-30, where it states:

“Once the write to the FIFO-O9 has been finished, the control part 5 stops enabling writes to the FIFO-O9, and then enables writes to the FIFO-E7. The control part 5 also simultaneously enables reads from the FIFO-E7 to start reading the written data therefrom.”

Yoshikawa teaches simultaneous reading and writing of the same signal to/from the same FIFO and then sequentially moves to the next FIFO. In the present invention, the first and second image signals of the left side and the right side of the processor are output simultaneously as shown at least in Figure 4 and described at least at page 7, lines 3-16.

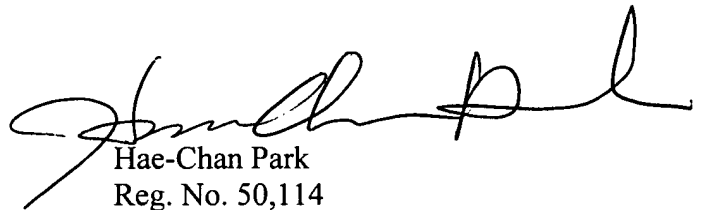
As to the dependent claims 2-8 and 10-12, Applicant submits that these claims depend from respective allowable independent claims 1 and 9, and accordingly are at least allowable due to this dependency. Accordingly, Applicants respectfully request that the 35 U.S.C. §103 rejection over claims 1-12 be withdrawn.

CONCLUSION

All of the stated grounds of objection and rejection have been properly traversed, accommodated, or rendered moot. Applicants therefore respectfully request that the Examiner reconsider all presently outstanding objections and rejections and that they be withdrawn. Applicants believe that a full and complete response has been made to the outstanding Office Action and, as such, claims 1-12 are in condition for allowance. If the Examiner believes, for any reason, that personal communication will expedite prosecution of this application, the Examiner is invited to telephone the undersigned at the number provided.

Prompt and favorable consideration of this Amendment is respectfully requested.

Respectfully submitted,



Hae-Chan Park  
Reg. No. 50,114

Date: January 30, 2004  
McGuireWoods LLP  
1750 Tysons Boulevard  
Suite 1800  
McLean, VA 22102-4215  
Tel: 703-712-5365  
Fax: 703-712-5280  
HCP:CJG  
\\COM207087.2